Task1 “Atmega32 drivers configured”:

**Objective:**

1-learning good driver writing

2-be familer with avr modules

**Responsibilities**:

1-Timer/counter 0 “8 bits” (heba)

2-Usart busy wait and with interrupts (mark)

3-Spi busy wait with interrupts (nourhan)

4-I2c (sameh)

5-Watchdog timer (karen)

6-Wifi (fady)

7-ADC (nada)

Deliver on Tuesday midnight 12:00 am.

Please use DIO.c and DIO.h to read or write on any register

And use 1 <<bit name conventional. (Make #define bit\_name bit\_number)

Deliverables: MODULE.c MODULE.h MODULE \_CFG.h MODULE \_LCFG.c MODULE.LCFG.h

There will be online meeting Monday from 10:00 pm to 11:10 pm

**Online meeting agenda :**

1. Discuss current progress (10 mins)
2. There will be time slice for each one to ask about anything or any problem he had faced
3. Giving next tasks (understanding os and getting the requirements from it)

Next task teams

1. Fady and sameh (femto os)
2. Mark and nourhan (ka os)
3. Nada, heba and karen (free rtos)

Next task deliver on Friday midnight 12:00 am

Deliverables: Detailed os requirements